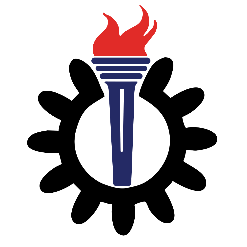


Design of a 32-bit RISC-V Core

Arvin Delavari – Faraz Ghoreishy



**I U S T**



GitHub.com/phoenix/phoenix-RV32